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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/725,663	12/01/2003	Radoslav Danilak	NVID-P001159	5113
7590 01/27/2006		EXAMINER		
WAGNER, MURABITO & HAO LLP			LEE, CHUN KUAN	
Third Floor Two North Market Street		ART UNIT	PAPER NUMBER	
San Jose, CA 95113			2181	
			DATE MAILED: 01/27/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Analizant/a)				
	Application No.	Applicant(s)				
Office Action Commence	10/725,663	DANILAK ET AL.				
Office Action Summary	Examiner	Art Unit				
	Chun-Kuan (Mike) Lee	2181				
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address				
, ,	/ IO OFT TO EVOIDE AMONTH!	O) OD TUBETY (20) DAYC				
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If NO period for reply is specified above, the maximum statutory period v  - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be timused and will expire SIX (6) MONTHS from a cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).				
Status						
1)⊠ Responsive to communication(s) filed on <u>01 N</u>	ovember 2005.					
3) Since this application is in condition for allowar	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under E	Ex parte Quayle, 1935 C.D. 11, 45	53 O.G. 213.				
Disposition of Claims						
4)⊠ Claim(s) <u>1,6-8,12 and 13</u> is/are pending in the application.						
4a) Of the above claim(s) <u>2-5, 9-11 and 14-20</u> is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1,6-8,12 and 13</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/o	r election requirement.					
Application Papers						
9) The specification is objected to by the Examine	ır					
10)⊠ The drawing(s) filed on <u>01 December 2003</u> is/a		ed to by the Examiner.				
Applicant may not request that any objection to the						
Replacement drawing sheet(s) including the correct	ion is required if the drawing(s) is ob	jected to. See 37 CFR 1.121(d).				
11)☐ The oath or declaration is objected to by the Ex	caminer. Note the attached Office	Action or form PTO-152.				
Priority under 35 U.S.C. § 119						
12)⊠ Acknowledgment is made of a claim for foreign	priority under 35 U.S.C. § 119(a)	)-(d) or (f).				
a) ⊠ All b) □ Some * c) □ None of:						
1.⊠ Certified copies of the priority documents have been received.						
2. Certified copies of the priority documents have been received in Application No						
3. Copies of the certified copies of the priority documents have been received in this National Stage						
application from the International Bureau	, ,,,					
* See the attached detailed Office action for a list	of the certified copies not receive	ed.				
Attachment(s)						
1) Notice of References Cited (PTO-892)	4) Interview Summary	(PTO-413)				
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Da					
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	6) Other:	ατοιπ πρφιισατιστί (ΕΤΟ*192)				

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## **DETAILED ACTION**

#### Election/Restrictions

1. In the previous office action, restriction to one of the following inventions was required under 35 U.S.C. 121:

- Claims 1-4 and 8-10 drawn to a system method of data transfer, classified in class 710, subclass 33.
- Claims 5 and 11 drawn to a system method of a status update notification,
   classified in class 710, subclass 19.
- III. Claims 6-7 and 12-13 drawn to a system method for controlling memory access to a storage device classified in class 711, subclass 154.

Applicant's election without traverse of claims 6-7 and 12-13 in the reply filed on 11/01/2005 is acknowledged. Examiner will examine claims 1, 6-8 and 12-13 on the merits, since claims 1 and 6 are generic claims.

Upon the allowance of a generic claim, applicant will be entitled to consideration of claims to additional species which are written in dependent form or otherwise include all the limitations of an allowed generic claim as provided by 37 CFR 1.141.

# Claim Rejections - 35 USC § 102

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1, 6-8 and 12-13 are rejected under 35 U.S.C. 102(b) as being anticipated by Chisholm et al. (US Patent 5,968,143).

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2. As per claims 1 and 8, <u>Chisholm</u> teaches a disk controller and a bridge

component for implementing efficient disk I/O for a computer system, comprising:

a bus interface (memory host controller 109 and I/O bridge 111 of Fig. 3) for interfacing with a processor (host processing unit 103 of Fig. 3) and a system memory (host memory 107 of Fig. 3) of the computer system (Fig. 3);

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a disk controller (local interface controller 113 of Fig. 1) for executing disk I/O transaction for the computer system, the disk controller further comprising:

a disk I/O engine (data/command block transfer controller 209 of Fig. 3) coupled to the bus interface (Fig. 3); and

a device interface (Fig. 2 ref 217) coupled to the disk I/O engine for interfacing the disk I/O engine with a disk drive (storage device 114 of Fig. 1), wherein the disk I/O engine is configured to cause a start up of the disk drive upon receiving a disk start up command (command transfer start signal) from the processor, the disk I/O engine further configured to execute a disk transaction by processing the disk transaction information (command block 304 of Fig. 3) from a bypass register (local memory 203 of Fig 3) coupled to the disk I/O engine (col.5 I. 1 to col. 6, I. 8).

3. As per claim 6, <u>Chisholm</u> teaches the disk controller and the bridge component for implementing efficient disk I/O for a computer system, comprising the disk controller further comprising a CPB pointer buffer (command address queue 309 of Fig 3) coupled to the disk I/O engine for dynamically appending a plurality of CPB pointers (addresses

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pointing to where the command blocks are stored) to extend to a number of disk transactions scheduled for execution by the disk I/O engine (col.5 I. 1 to col. 6, I. 8).

4. As per claim 7, <u>Chisholm</u> teaches a disk controller and bridge component for implementing efficient disk I/O for a computer system, comprising the disk controller further comprising a chain memory (local command address images 309 of Fig 3, wherein the addresses are subsequently stored and retrieved in a chain) coupled to the disk I/O engine for buffering a plurality of CPBs to extend to a number of disk transactions scheduled for execution by the disk I/O engine (col.5 I. 1 to col. 6, I. 8).

Claims 12-13 repeat the limitations of claims 6-7 and are therefore rejected accordingly.

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## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Chun-Kuan (Mike) Lee whose telephone number is (571)272-0671. The examiner can normally be reached on 8AM to 5PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kim N. Huynh can be reached on (571)272-4147. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

C.K.L. 01/23/2006

> KIM HUYNH SUPERVISORY PATENT EXAMINER

> > 1/23/05